


Ternary content-addressable memory with fast error detection (Technion)

code: COM-1104

Ternary content-addressable memory (TCAM) devices are increasingly being used for performing high-speed classification of data packets required by networking applications such as routing, monitoring and security. The parallel-access properties of TCAM devices make them prone to errors which standard error detection schemes are not designed to handle. Our technology is a parallel error detection scheme which is optimized for TCAM. It allows an order of magnitude reduction in the number of lookups required for the error location process, thereby increasing performance and reliability.

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